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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:	Girish Radhakrish Kamath	Group No.:	Not yet assigned
Serial No.:	Not yet assigned	Atty. Docket No.:	37125/0110/31374
Filed:	2/7/02		
For:	Autotransformer-Based System and Method of Current Harmonics Reduction in a Circuit	Examiner:	Not yet assigned

Commissioner of Patents and Trademarks
Washington, DC 20231

PRELIMINARY AMENDMENT

Please amend the referenced patent application as follows:

AMENDMENT

In the specification:

Please add the following sentence after the title of the present application:

CROSS REFERENCE TO RELATED APPLICATIONS

This application is a divisional application of patent application serial number 09/818,068 entitled "Autotransformer-based system and method of current harmonics reduction in a circuit" filed on March 27, 2001.

In the claims:

1. (Delete) A system for reducing harmonics in a circuit, the circuit being powered by a main three phase power source having a main three phase voltage set, each main phase voltage having a main voltage amplitude and a main voltage phase, the system comprising:

main rectifier means;

auxiliary rectifier means connected to the main rectifier means; and

an autotransformer connected to the main rectifier means and the auxiliary rectifier means, the autotransformer adapted to generate a set of auxiliary voltages, each auxiliary